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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/880699
Filing Date	June 13, 2001
First Named Inventor	Takahashi, Richard
Group Art Unit	2131
Examiner Name	Unknown

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ORIGINALLY FILED

Sheet 1 of 1

Attorney Docket No: 01504.002US1

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
JA	US 2002/0001384 A1	01/03/2002	Buer, Mark, et al	380	30	04/04/2001

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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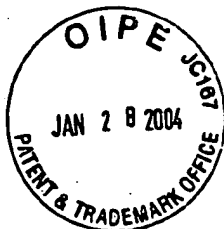
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DATE CONSIDERED

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\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached



Sheet 1 of 1

Form 1449\*

Atty. Docket No.: 4224-20US1

Serial No. 09/880699

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BY APPLICANT**

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Applicant: Richard Takahashi

Filing Date: 6/13/01

Group: 2131

**U.S. PATENT DOCUMENTS**

**EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

**EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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**OTHER DOCUMENTS****\*\*EXAMINER  
INITIAL** (Including Author, Title, Date, Pertinent Pages, Etc.)

or BOSSELAERS A ET AL: "SHA: A Design for Parallel Architectures?"  
ADVANCES IN CRYPTOLOGY. EUROCRYPT 97, 25 February 1997 (1997-02-25),  
pages 1-15; Konstanz, Germany.

or SHERIGAR M B ET AL: "A pipelined parallel processor to implement MD4 message  
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IEEE COMPUT. SOC, US, 4 January 1998 (1998-01-04), pages 394-399.

or ARNOLD J M: "Mapping the MD5 hash algorithm onto the NAPA architecture"  
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SYMPOSIUM ON NAPA VALLEY, CA, USA 15-17 APRIL 1998, LOS  
ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 15 April 1998 (1998-04-15),  
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Examiner

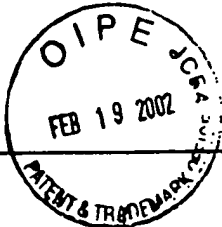
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Form 1449*	Atty. Docket No.: 1504.002US1	Serial No. 09/880,699
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Richard J. Takahashi	
	Filing Date: June 13, 2001	Group: 2131

## U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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## FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No

## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial							
OK	Schneier, B., <u>Applied Cryptography: Protocols, Algorithms, and Source Code in C, Second Edition</u> , John Wiley & Sons, Inc., pp. 435-441, (1996)						

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